



Title: Draft Wafer Level Capped Sensor
Inventor(s): Felton et al.
Appl No. 10/657,741
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Docket No. 2550/176:
Page 1 of 5

1/5

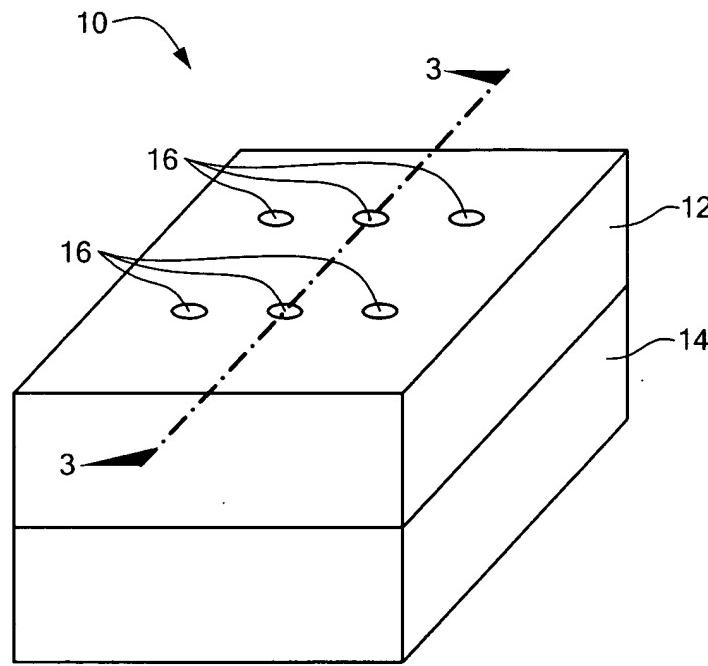


FIG. 1

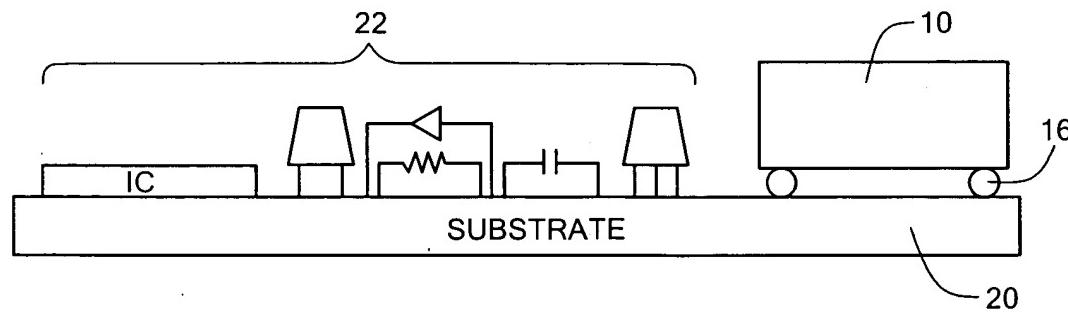


FIG. 2

2/5

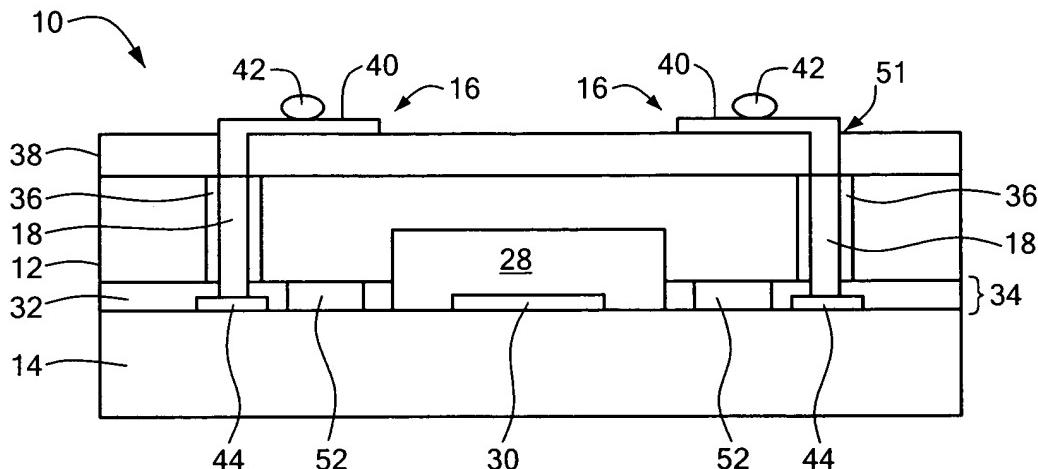


FIG. 3

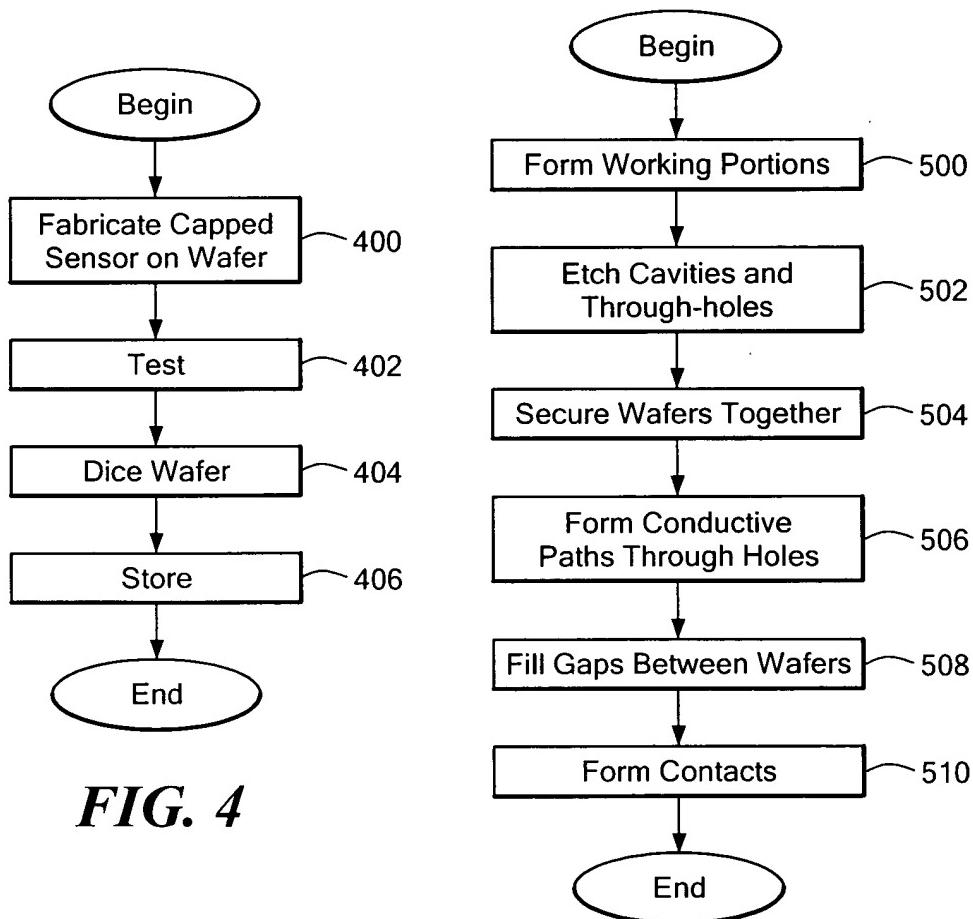


FIG. 4

FIG. 5

3/5

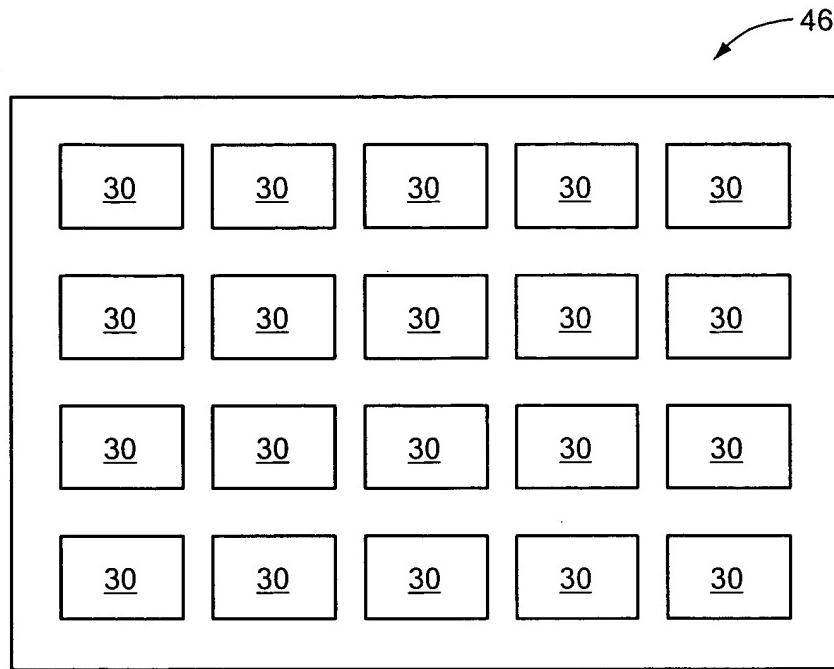


FIG. 6

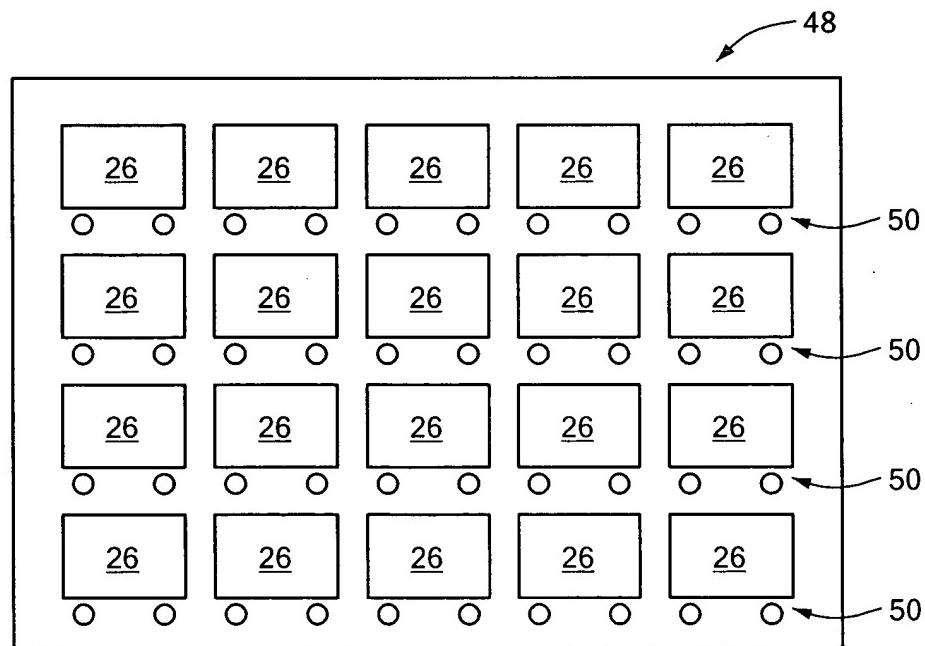


FIG. 7

4/5

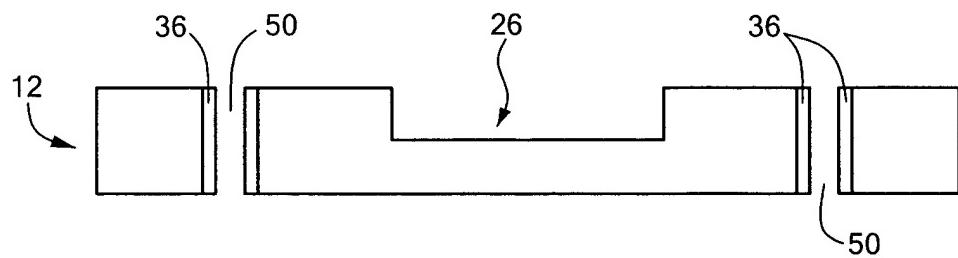


FIG. 8A

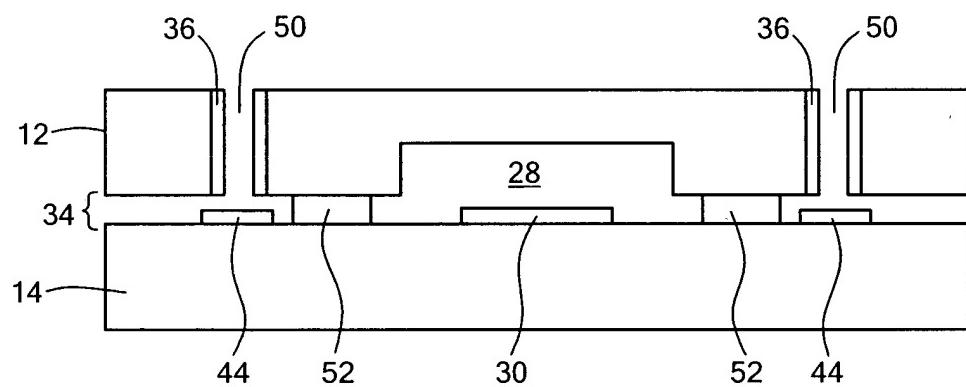


FIG. 8B

5/5

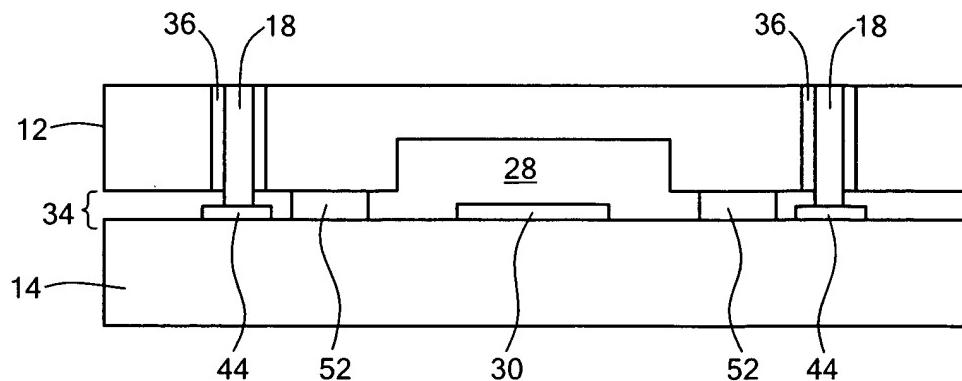


FIG. 8C

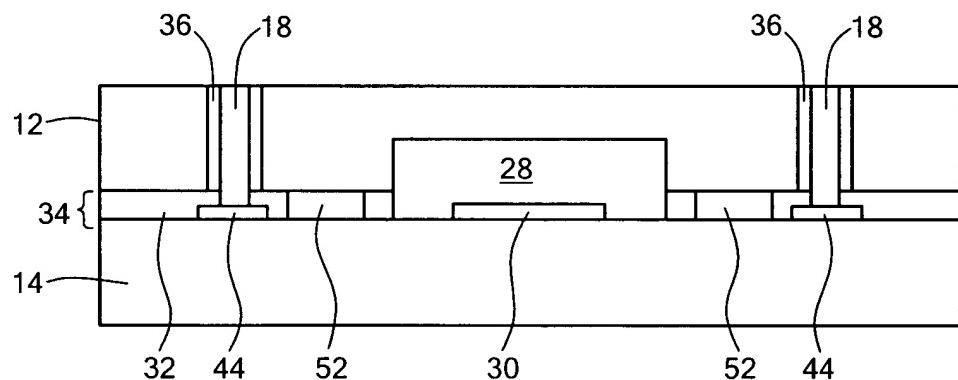


FIG. 8D